

IN THE SPECIFICATION

Please replace the paragraph beginning at page 11, line 22, with the following:

In summary, the output of the phase detector 106 and the charge pump 108 is averaged by the input capacitance of the stabilization filter 110 and is then sampled to the hold capacitor ~~182~~ 224. The stabilization filter 110 not only stabilizes the closed loop system but also provides the average of multiple phase errors. Unlike the conventional sampling technique, the disclosed invention does not require a dedicated integration capacitor driven by charge pump to average out phase error.

Please replace the paragraph beginning at page 20, line 5, with the following:

At step 606, the phase detector 106 compares the reference clock signal 132 to the internal clock signal 134. At step 608, the phase detector 106 generates an up signal 136 ~~[[and/]]~~ or a down signal 138 based on the comparison of the reference clock signal 132 to the internal clock signal 134. At step 610, the charge pump 108 generates a charge pump output signal 140 based on the up and down signals 136 and 138. The charge pump 108 sources current based on the up signal 136 and sinks current based on the down signal 138 to generate the charge pump output signal 140.